



10-31-03

2814

First Named Inventor	D. Mark Durcan	TRANSMITTAL FORM UNDER 37 CFR 1.10 (LARGE ENTITY)
Serial No.	09/652,998	
Filing Date	August 31, 2000	
Group Art Unit	2814	
Examiner Name	Anh D. Mai	
Attorney Docket No.	400.153US07	
Title: CONTAINER CAPACITOR STRUCTURE AND METHOD OF FORMATION THEREOF		

Mail Stop: Non-Fee Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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Enclosures							
The following documents are enclosed:							
<input checked="" type="checkbox"/>	A Response to Office Action (6 pgs.) and "Appendix A" (1 sheet);						
<input checked="" type="checkbox"/>	A return postcard.						
Please charge any additional fees or credit any overpayments to Deposit Account No. 501373.							
<u>CUSTOMER NUMBER 27073</u>							
Leffert Jay & Polglaze, P.A. P. O. Box 581009 Minneapolis, MN 55458-1009							
Fee Calculation							
	Number of Claims		Prv. Amt.	Extra Claims		Fee	Fee Paid
Total Claims		-20		0	X	\$18	=
Independent Claims		-3		0	X	\$84	=
							=
Total							
Submitted By							
Name	Daniel J. Polglaze	Reg. No.	39,801	Telephone	(612) 312-2203		
Signature				Date	October 30, 2003		
Certificate of Mailing							
"Express Mail" mailing label number: EL963841046US Date of Deposit: October 30, 2003							
These papers and fees are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.							

(LARGE ENTITY TRANSMITTAL UNDER 37 C.F.R. 1.10)



S/N 09/652,998

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named

Inventor: D. Mark Durcan

Examiner: Anh D. Mai

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THEREOF

RESPONSE

Commissioner for Patents
P. O. Box 1450
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In response to the Office Action dated July 30, 2003, Applicant responds as follows:

In the Claims

Claims 1 - 12 (cancelled)

13. (currently amended) An array of capacitors, comprising:
- a first bottom capacitor plate;
 - a second bottom capacitor plate;
 - a third bottom capacitor plate;
 - a contact between said first bottom capacitor plate and said second bottom capacitor plate, the contact downwardly extends from a vertical height above a top of the first bottom capacitor plate;
 - a trench between said second bottom capacitor plate and said third bottom capacitor plate;
 - a common top capacitor plate over said first bottom capacitor plate, said second bottom capacitor plate, and said third bottom capacitor plate, wherein said top capacitor plate extends toward said contact at a first level within said array, the top capacitor plate includes a lateral clearance opening at the first level around the contact and does not vertically descend between the contact and any each of the first, second and third bottom capacitor plates ~~and the contact~~, and wherein said top capacitor plate lines a side of said trench and further lines a bottom of said

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